



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gerd Frankowsky et al. Art Unit : 2811
Serial No. : 10/630,632 Examiner : Unknown
Filed : July 29, 2003
Title : SEMICONDUCTOR CIRCUIT MODULE AND METHOD FOR FABRICATING
SEMICONDUCTOR CIRCUIT MODULES

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PETITION FOR TWO-MONTH EXTENSION OF TIME

Pursuant to 37 CFR §1.136, applicant hereby petitions that the period for response to the action dated October 28, 2003, be extended for two months to and including February 28, 2004.

Enclosed is a check for \$420 for the required fee. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date:

2/25/04

Faustino A. Lichauco
Reg. No. 41,942

Fish & Richardson P.C.
225 Franklin Street
Boston, MA 02110-2804
Telephone: (617) 542-5070
Facsimile: (617) 542-8906

20812337.doc

03/03/2004 HBERHE 00000061 10630632

04 FC:1252

420.00 OP

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

February 25, 2004
Date of Deposit

Signature

Iria Zarembok

Typed or Printed Name of Person Signing Certificate